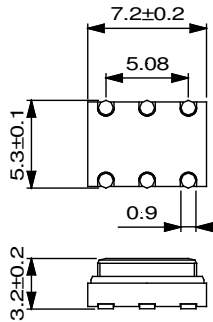


MINIATURE HIGH FREQUENCY SURFACE MOUNT VCXO

DFV S2-LEC (3.3 V)

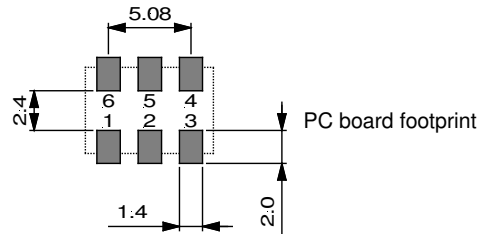
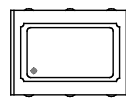
KEY FEATURES
16 to 200 MHz
Encapsulated crystal
1 ps RMS jitter over 50 kHz to 80 MHz B.W.
APPLICATIONS
Sonet/SDH/Datacomms



Marking:

DFV S2-LE
Frequency
Code yrwk

Function	DFV S2
V control	1
E / D	2
GND	3
Output 1	4
Output 2	5
Vcc	6



TYPE	DFV S2-LECPI
Frequency Range	16 to 200 MHz

ELECTRICAL SPECIFICATIONS	
supply voltage	3.3 V ± 5 %
supply current (no load)	≤ 60 mA
output load	PECL 100 K (50 Ω to 1.3 V)
duty cycle @ 50% level	40/60...60/40 %
rise/fall times (20 to 80%)	≤ 0.5 ns
high/low levels	≥ 2.22 V / ≤ 1.7 V
jitter RMS (12 kHz to 5 MHz)	≤ 0.5 ps
jitter RMS (50 kHz to 80 MHz)	≤ 1 ps (f = 155.52 MHz)
enable / disable on pin 2	low or open = enable, high = disable
complementary output on pin 5	180° phase shifted
start up	≤ 10 ms @ 3.15 V

FREQUENCY STABILITY			detailed tolerances [ppm]						
type	temperature range	model code	stability versus:				pulling range		control voltage
			temp.	@ 25°C	Vcc	load	ageing	positive function	
DFV S2-LE	0 to 70°C	100B15	≤ ± 15	≤ ± 10	≤ ± 3	≤ ± 0.5	≤ ± 2	≥ ± 100	1.65 V ± 1.5 V
		100B25	≤ ± 25						
remarks			input impedance ≥ 10 kΩ, modulation bandwidth ≥ 10 kHz @ -3dB ageing is 1 st year at 25°C						

ORDERING CODE	type + option code + frequency + model code
Example	DFV S2-LECPI 155.52 MHz 100B15

REMARK	Please consult factory for -40 to +85°C versions
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